

Fig.1A PRIOR ART

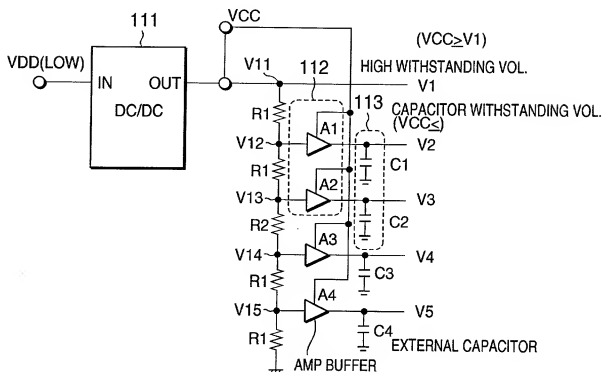


Fig.1B

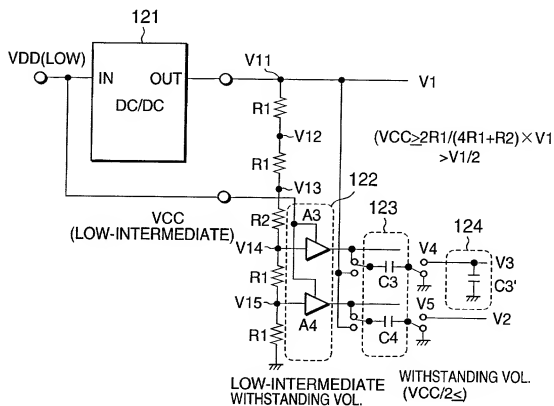




Fig.3 PRIOR ART

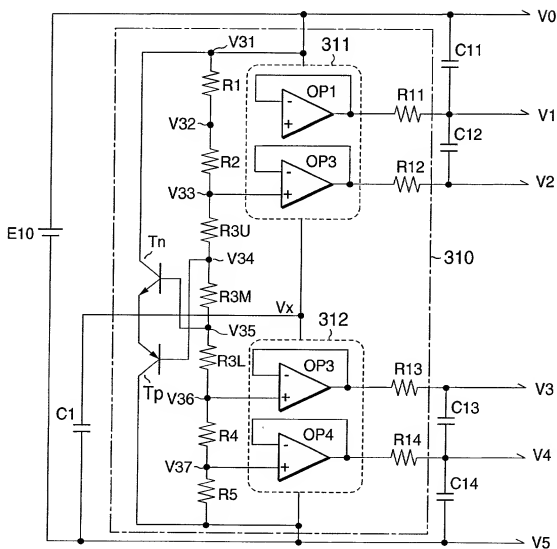


Fig.4 PRIOR ART

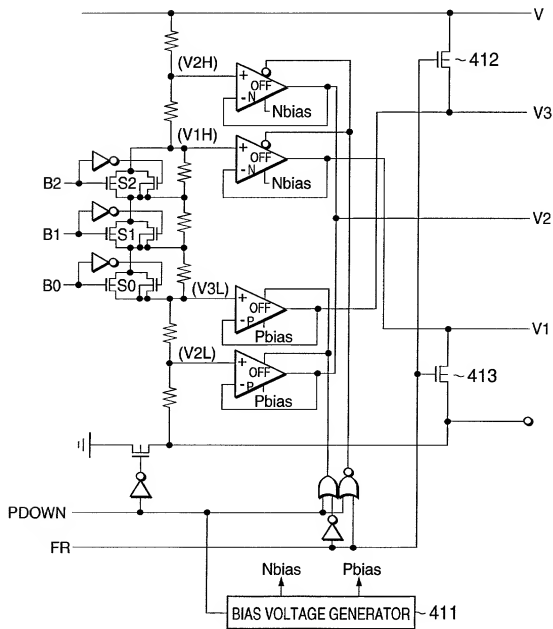


Fig.5

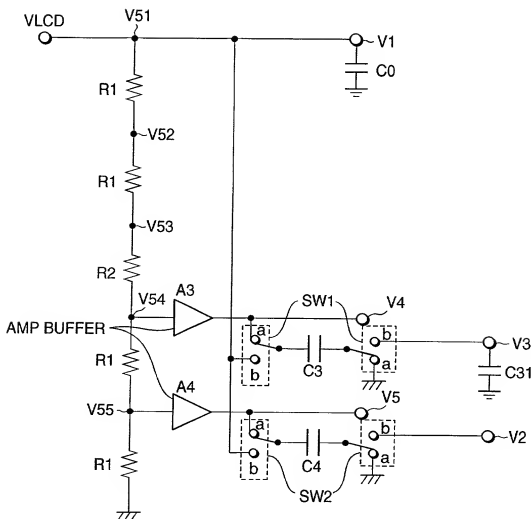
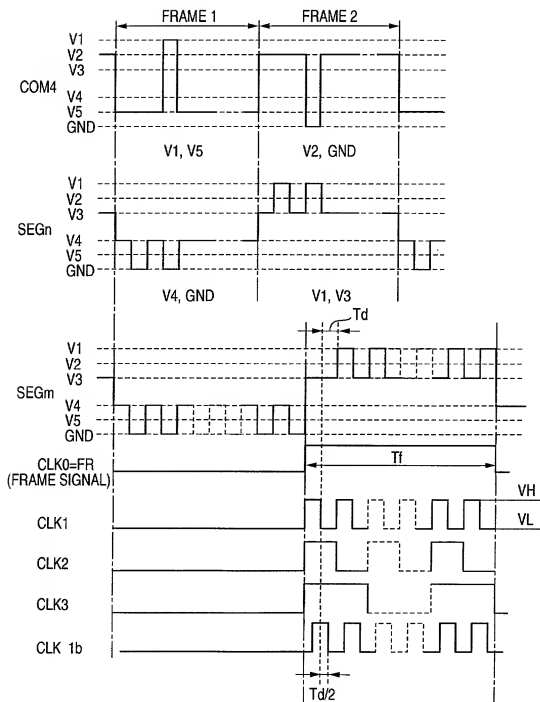


Fig.6



The diagram shows a 16-bit DAC circuit. It consists of two 8-bit DACs, A1 and A2, which are connected to a common output line V1. The input VLCD is connected to V71, which is also connected to V1. The circuit is divided into two sections, each with an 8-bit DAC and a switch. The first section (top) has an 8-bit DAC A1 with inputs V72, V73, and V74. The second section (bottom) has an 8-bit DAC A2 with inputs V75, V76, and V77. The outputs of A1 and A2 are connected to V2 and V3, respectively. The switches SW1 and SW2 are connected to V2 and V3, and their common terminals are connected to V1. The circuit also includes resistors R1, R2, and R3, capacitors C1 and C2, and a feedback capacitor C3.

Fig.8

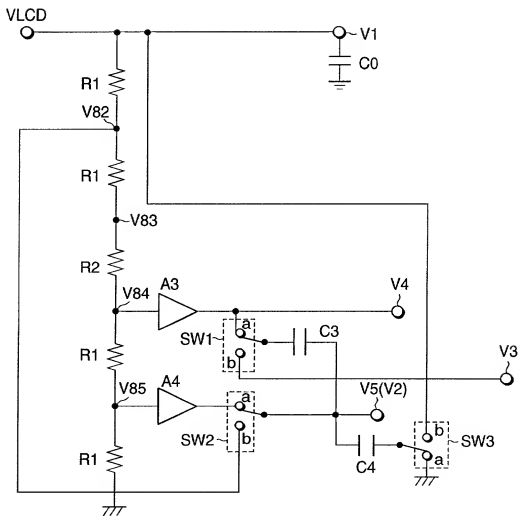




Fig.9

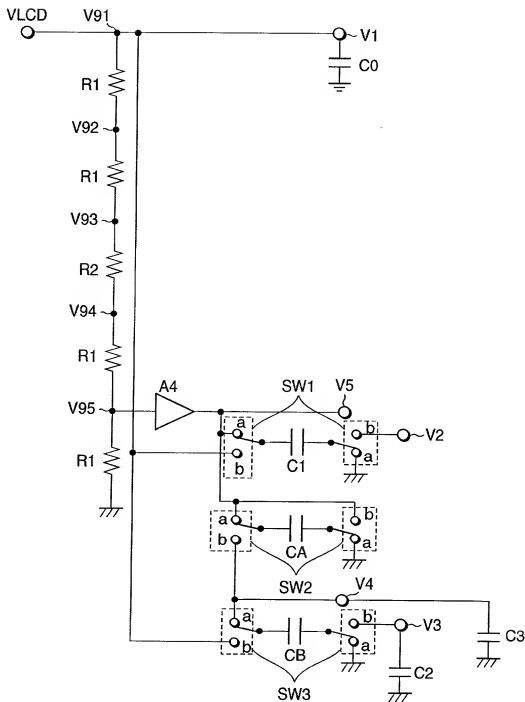


Fig.10

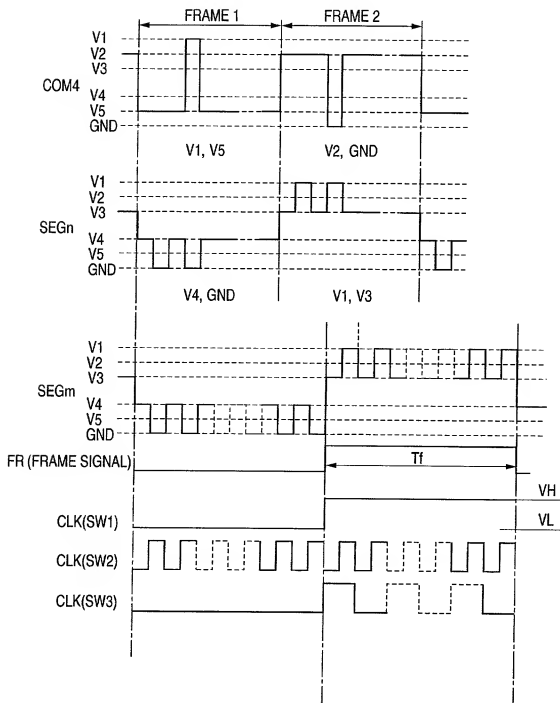


Fig.11

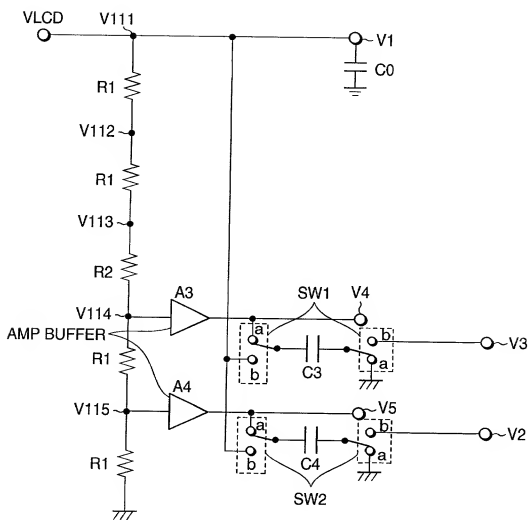


Fig.12

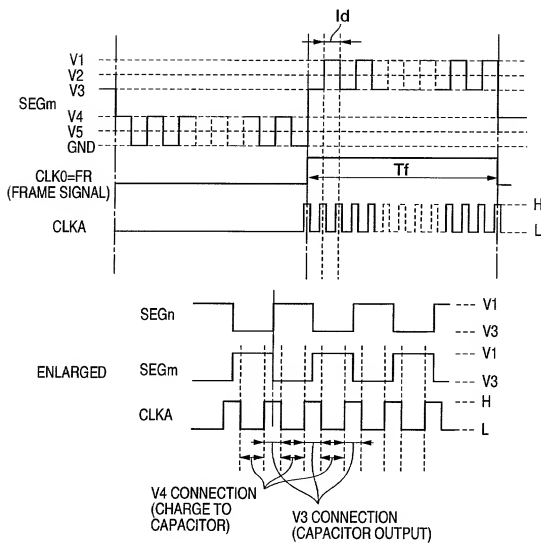


Fig.13

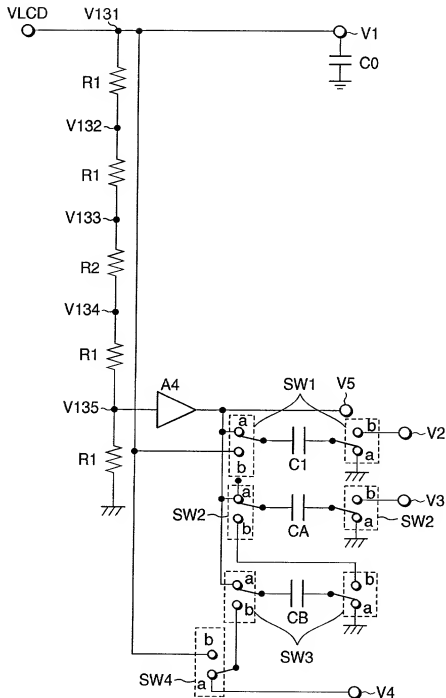


Fig.14

